

In the claims:

Please amend the claims as follows.

1-7. (Cancelled)

8. (Currently Amended) A voltage reference generator circuit according to claim 7~~9~~, wherein the current generated by the current generating circuit is supplied through first and second compensation resistors.

9. (Original) A voltage reference generator circuit comprising:
a first bipolar transistor connected in series with a resistive chain between upper and lower supply rails and having an input node at its base;
a current generating circuit connected to supply a current to a node of said resistive chain, said resistive chain including a compensation resistor connected between said node and said lower supply rail;
voltage generating means for generating a voltage proportional to absolute temperature across a current setting resistor of said resistive chain;
wherein the resistive value of the compensation resistor is selectable independently of the values of other components in the resistive chain, whereby an offset voltage across said compensation resistor is independently settable.

10-13. (Cancelled)

14. (Currently Amended) ~~The voltage generator of claim 12~~
A voltage generator, comprising:
an offset circuit operable to develop an offset voltage and operable to adjust the offset value as a function of temperature;
a voltage generation circuit coupled to the offset circuit, the voltage generation circuit operable to develop a first reference voltage and adjust the value of the first reference voltage as a function of temperature, and operable to provide an output reference voltage equal to the first reference voltage plus the offset voltage;
wherein the voltage generation circuit includes,

a bipolar transistor having a base-emitter voltage that is a function of temperature;

the offset circuit includes a bipolar transistor having a base-emitter voltage that is a function of temperature; and

wherein the voltage generation circuit includes,

a resistor network coupled between an emitter of the bipolar transistor and a node;

the offset circuit comprises a resistive element having a first terminal coupled to the node and a second terminal adapted to receive a reference voltage; and

wherein the offset circuit further comprises:

a current source having a first terminal adapted to receive a supply voltage and a second terminal; and

a resistive element having a first terminal coupled to a base of the bipolar transistor and a second terminal coupled to the node; and

wherein a collector and the base of the bipolar transistor are coupled to the second terminal of the current source and an emitter of the bipolar transistor is adapted to receive a reference voltage.

15. (Previously Presented) An integrated circuit, comprising:

a voltage generator, comprising,

an offset circuit operable to develop an offset voltage and operable to adjust the offset value as a function of temperature, wherein the offset circuit includes a bipolar transistor having a base-emitter voltage that is a function of temperature, and

a voltage generation circuit coupled to the offset circuit, the voltage generation circuit operable to develop a first reference voltage and adjust the value of the first reference voltage as a function of temperature, and operable to provide an output reference voltage equal to the first reference voltage plus the offset voltage, wherein the voltage generation circuit includes a bipolar transistor having a base-emitter voltage that is a function of temperature;

wherein the voltage generation circuit includes,

a first bipolar transistor connected in series with a resistive chain between upper and lower supply rails and having an input node at its base;

voltage generating means for generating a voltage proportional to absolute temperature across a current setting resistor of said resistive chain; and

wherein the offset circuit includes,

a current generating circuit connected to supply a current to a node of said resistive chain, said resistive chain including a compensation resistor connected between said node and said lower supply rail; and

wherein the resistive value of the compensation resistor is selectable independently of the values of other components in the resistive chain, whereby an offset voltage across said compensation resistor is independently settable.

16. (Original) The integrated circuit of claim 15 wherein the integrated circuit comprises a memory device.

17-29. (Cancelled)